

FIG. 1

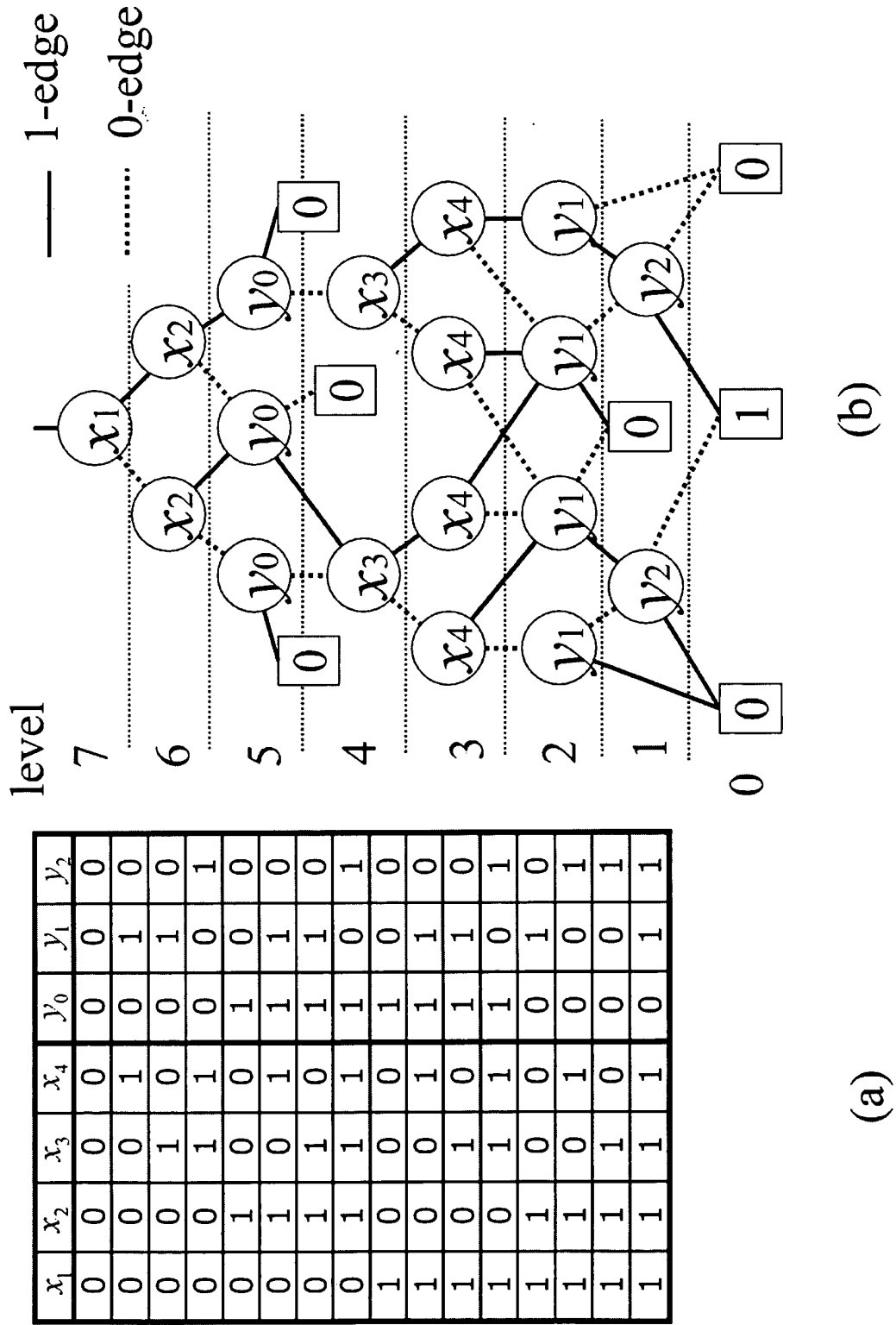


FIG. 2

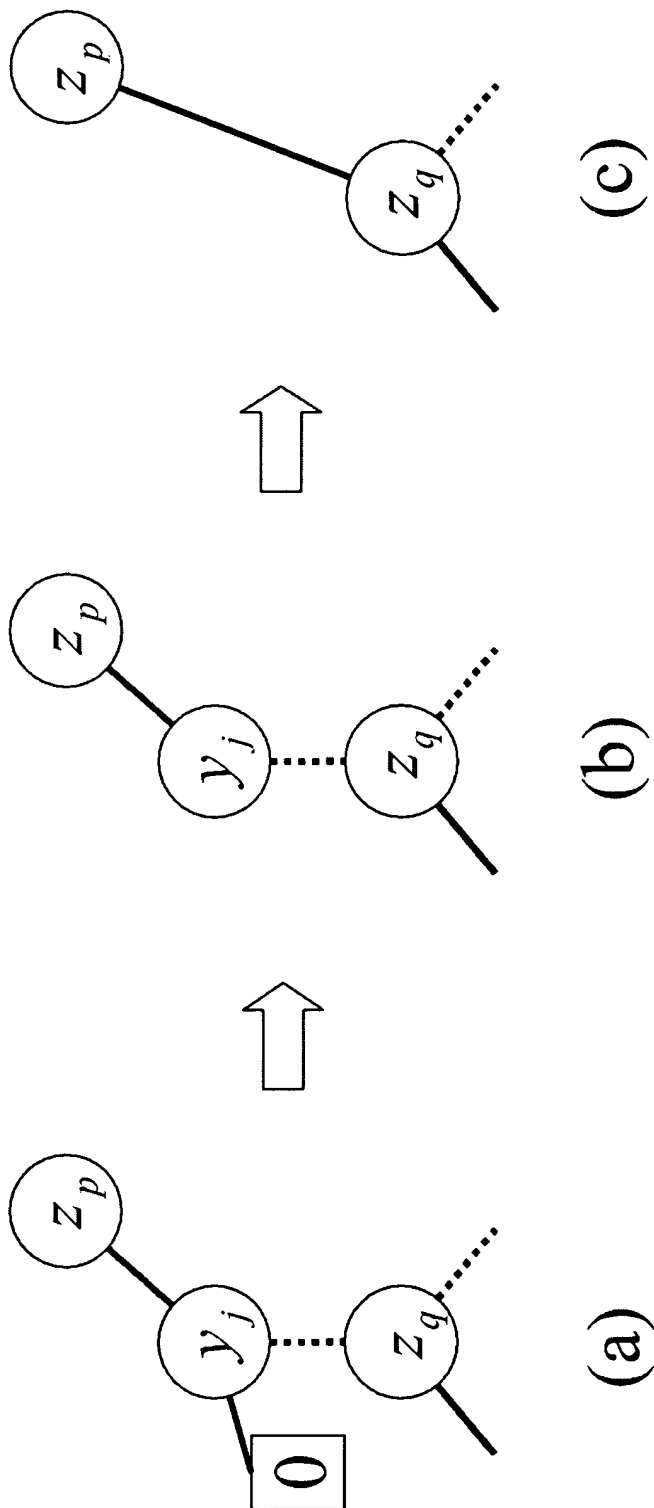


FIG. 3

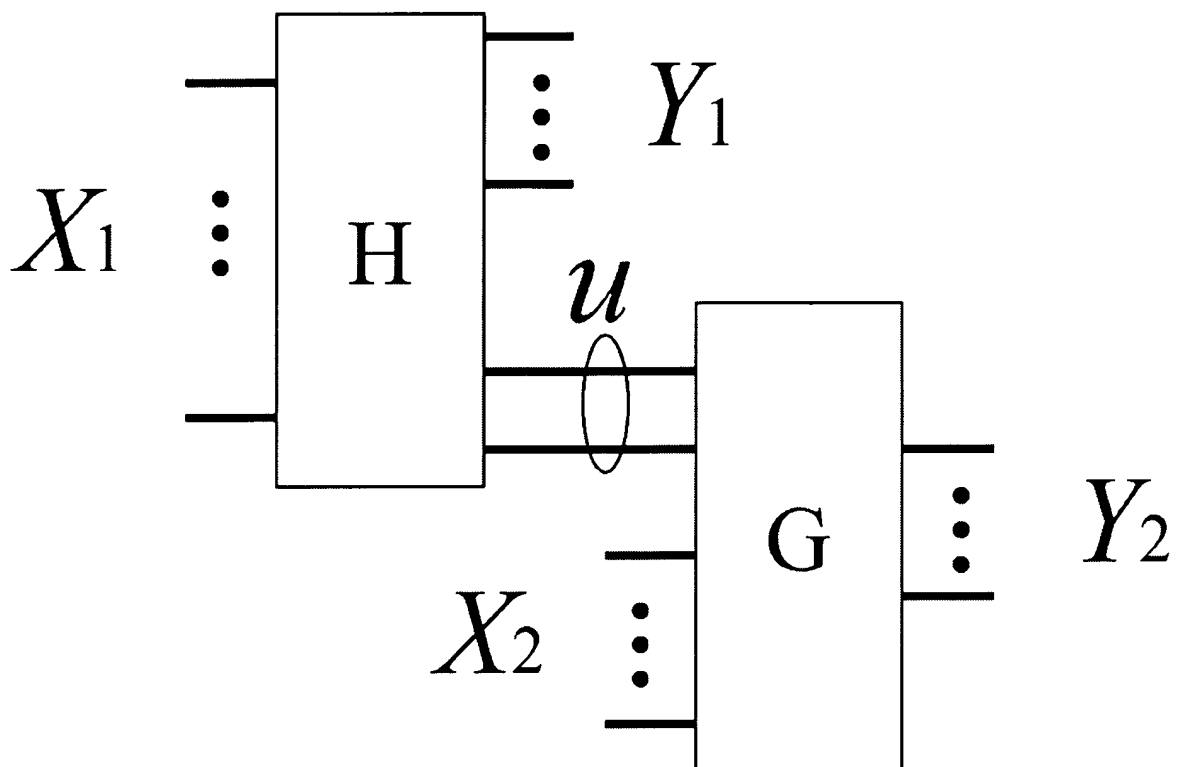


FIG. 4

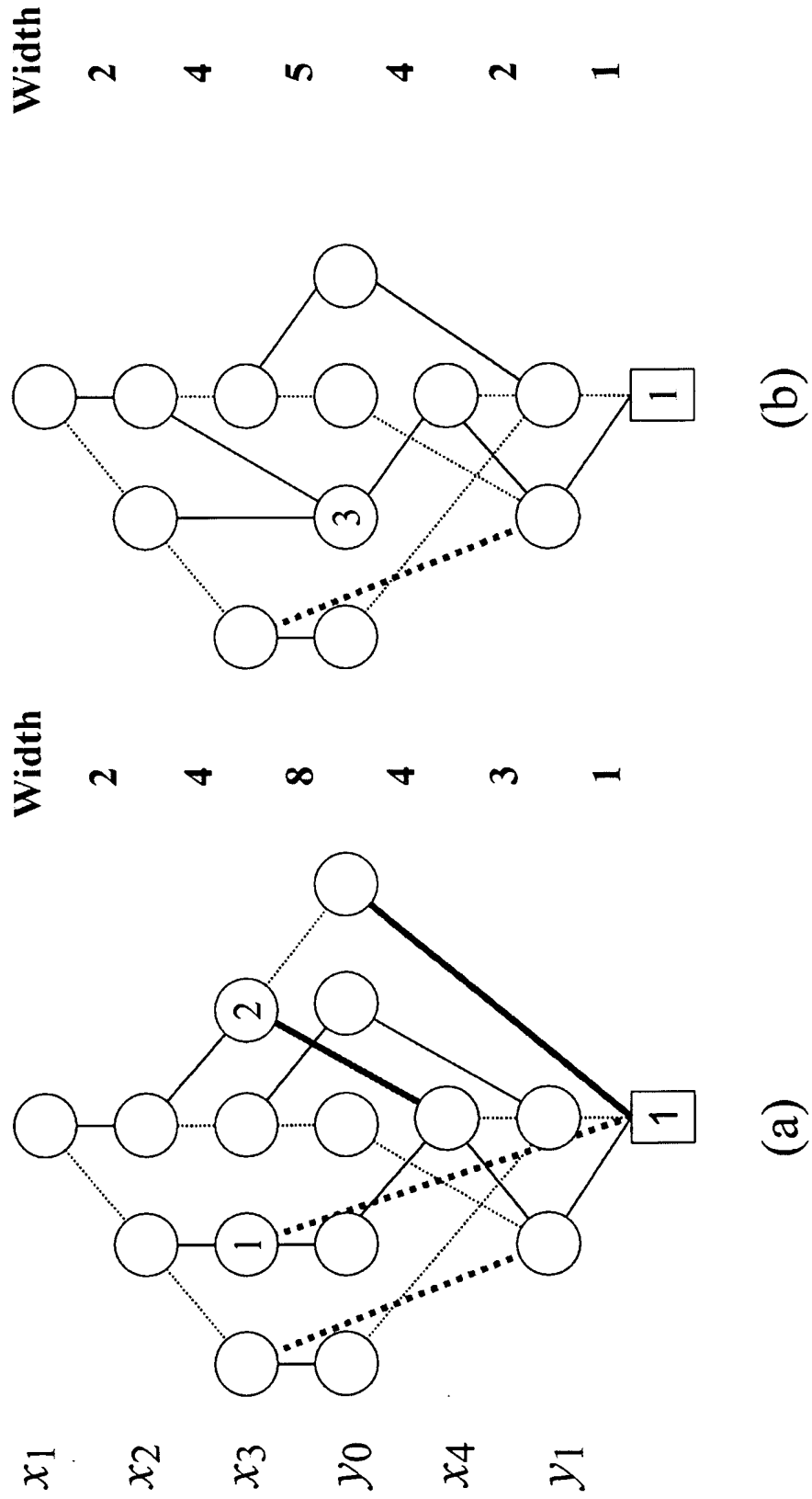


FIG. 5

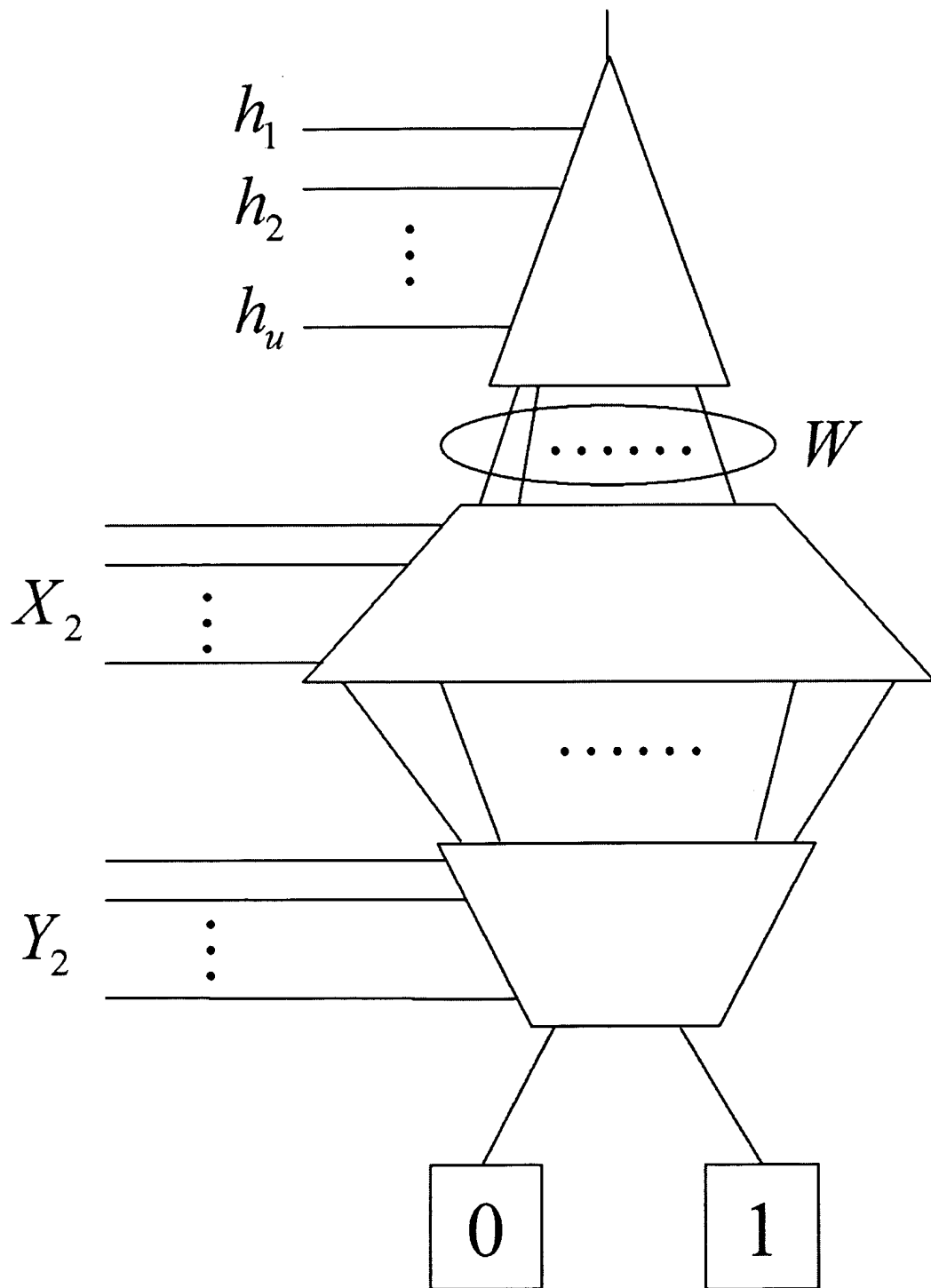


FIG. 6

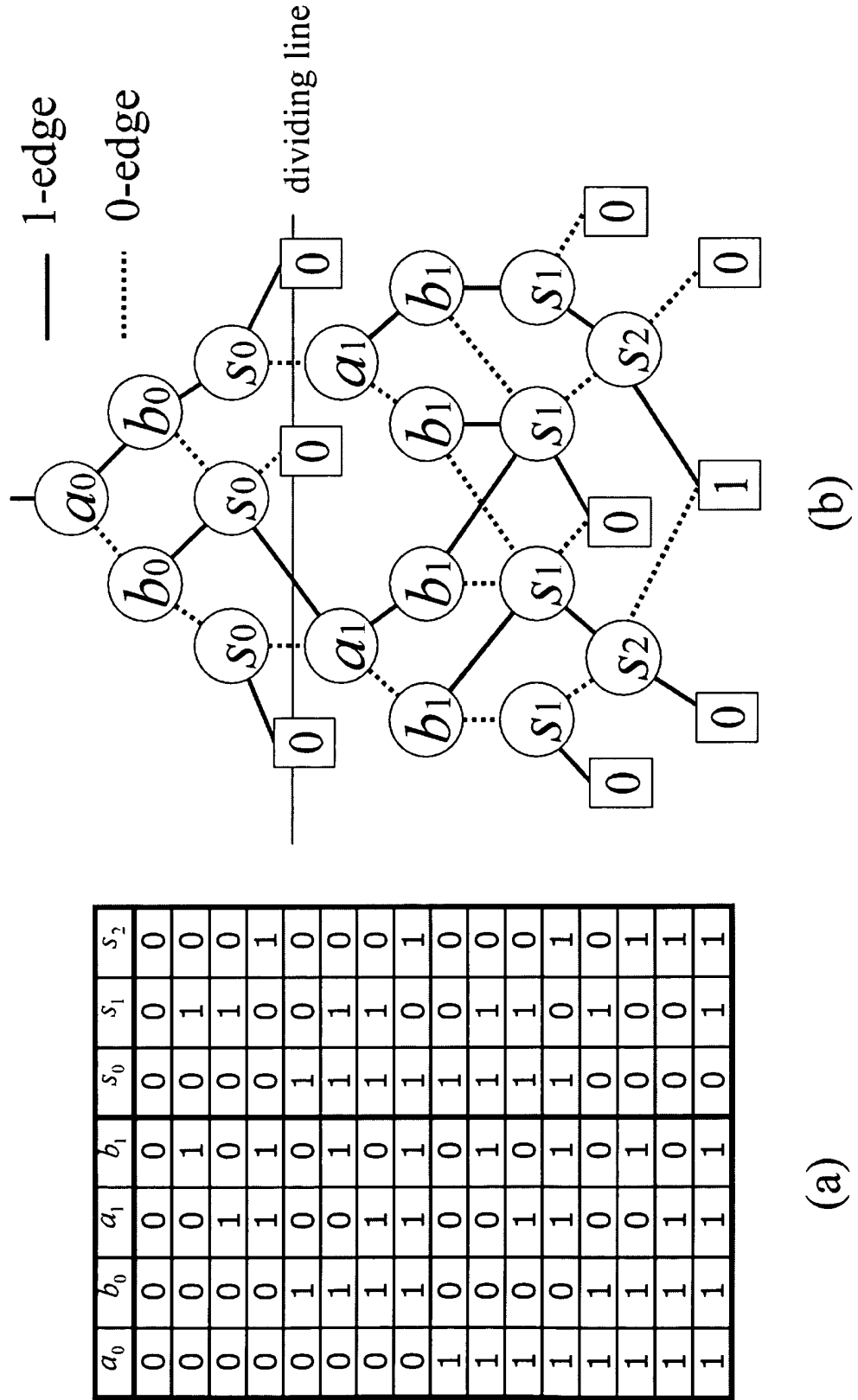


FIG. 7

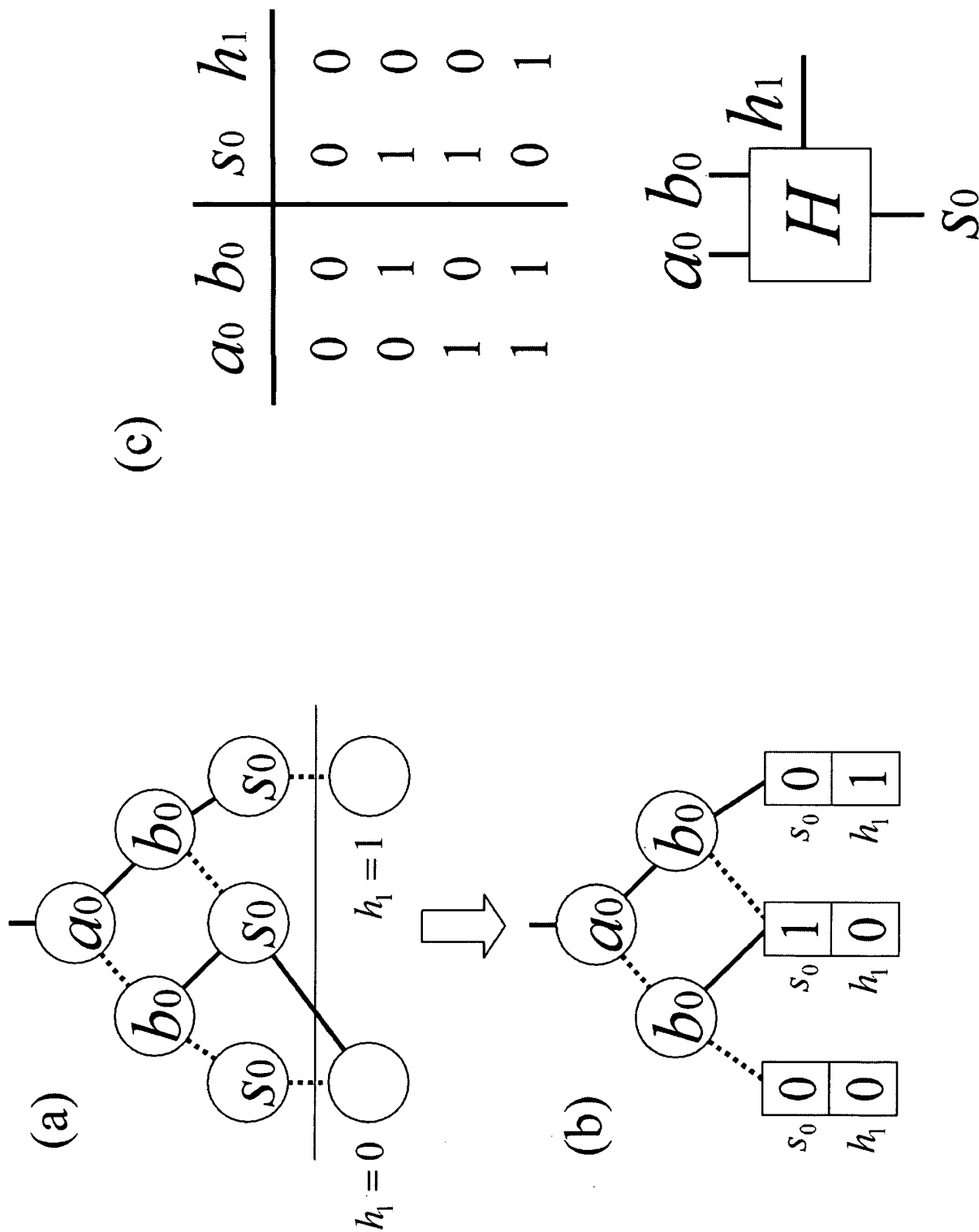


FIG. 8

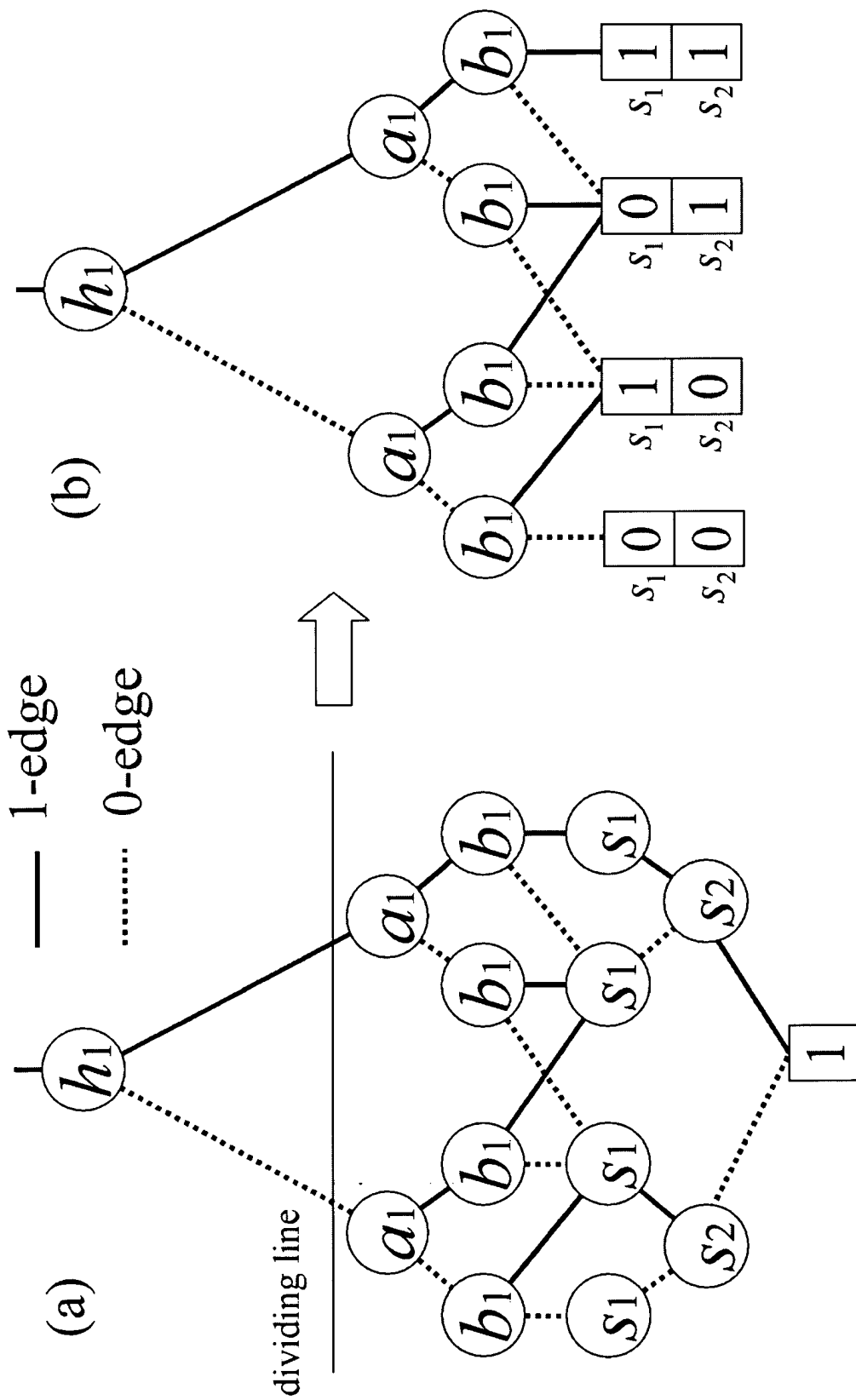


FIG. 9

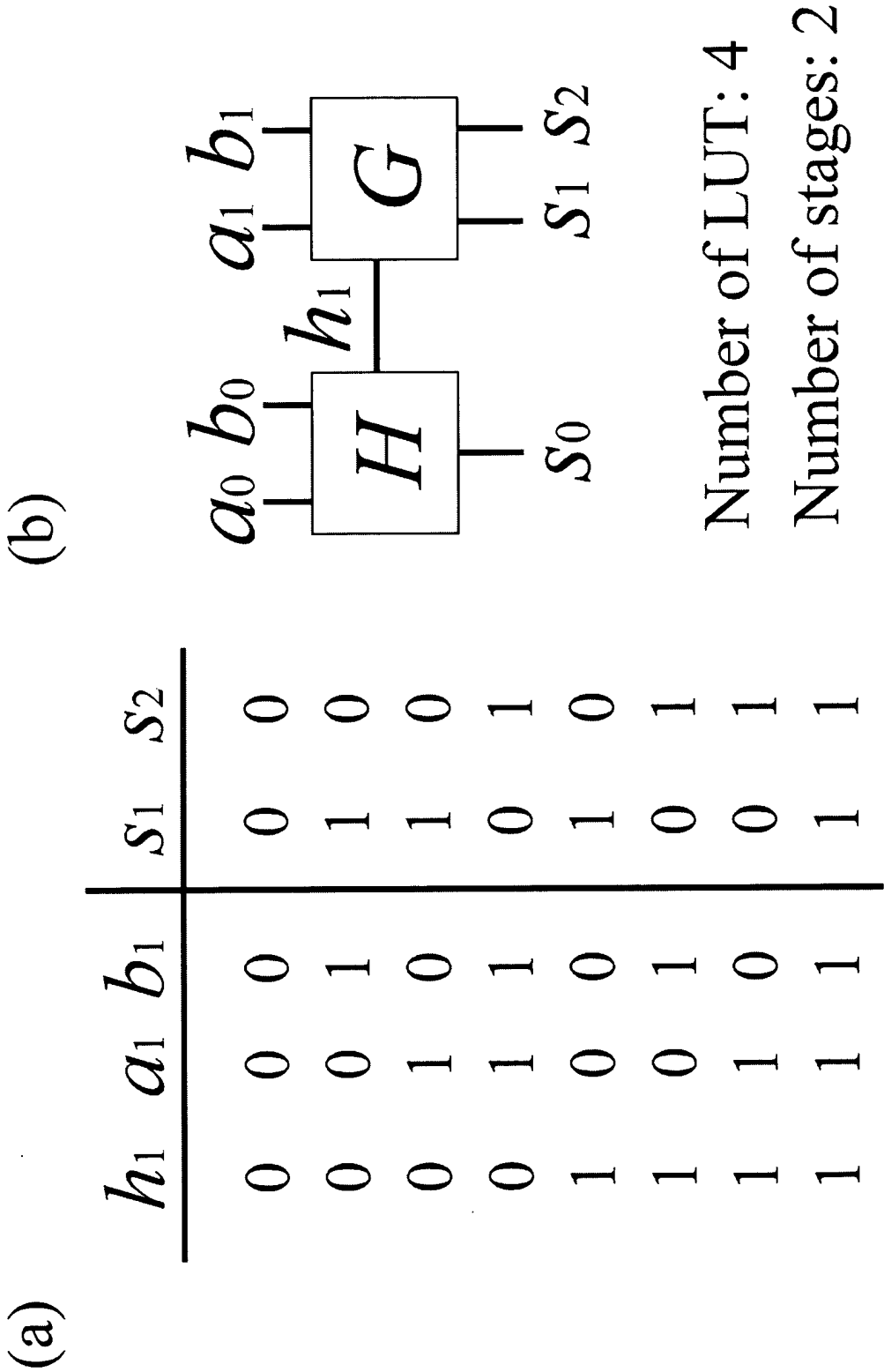
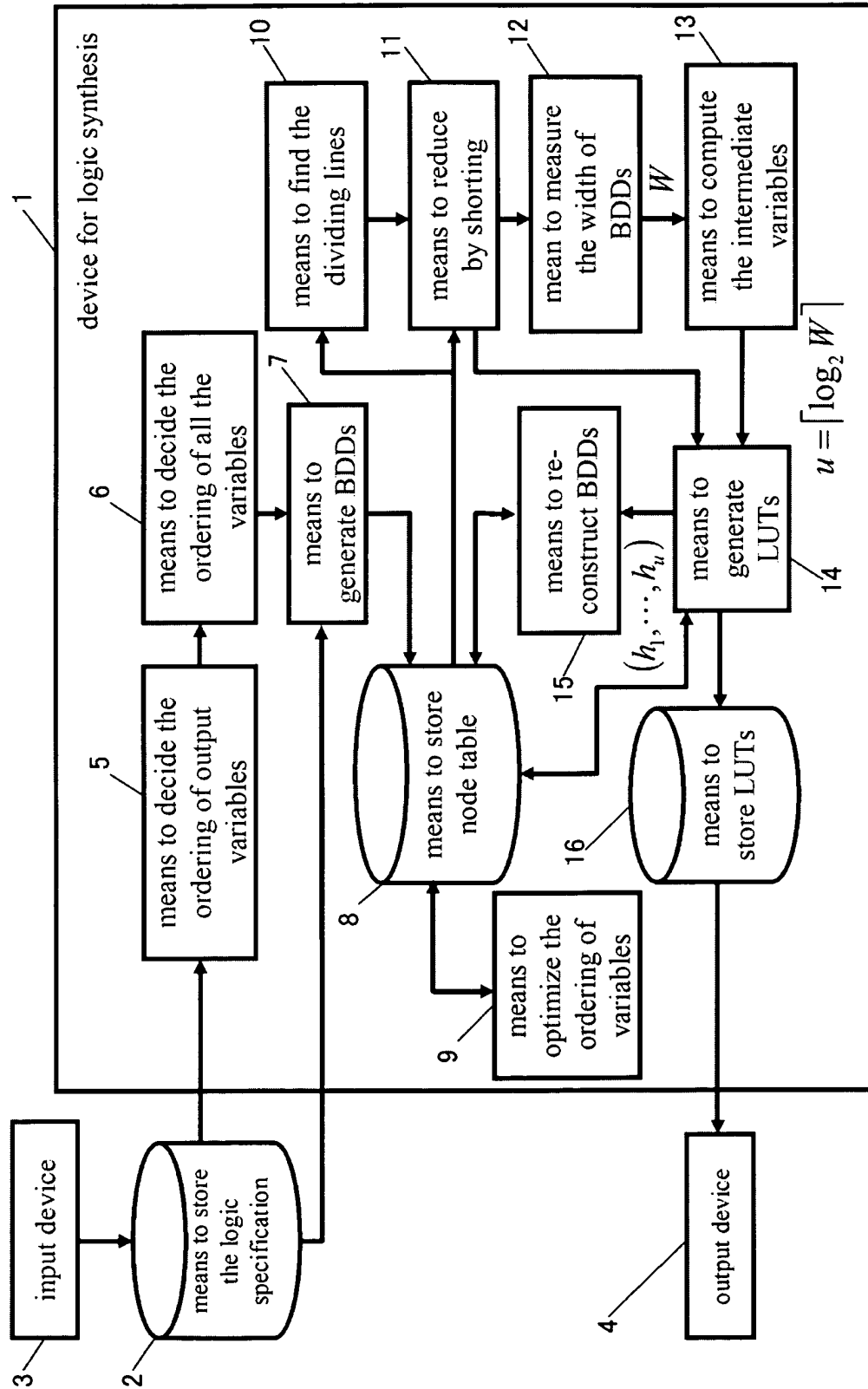
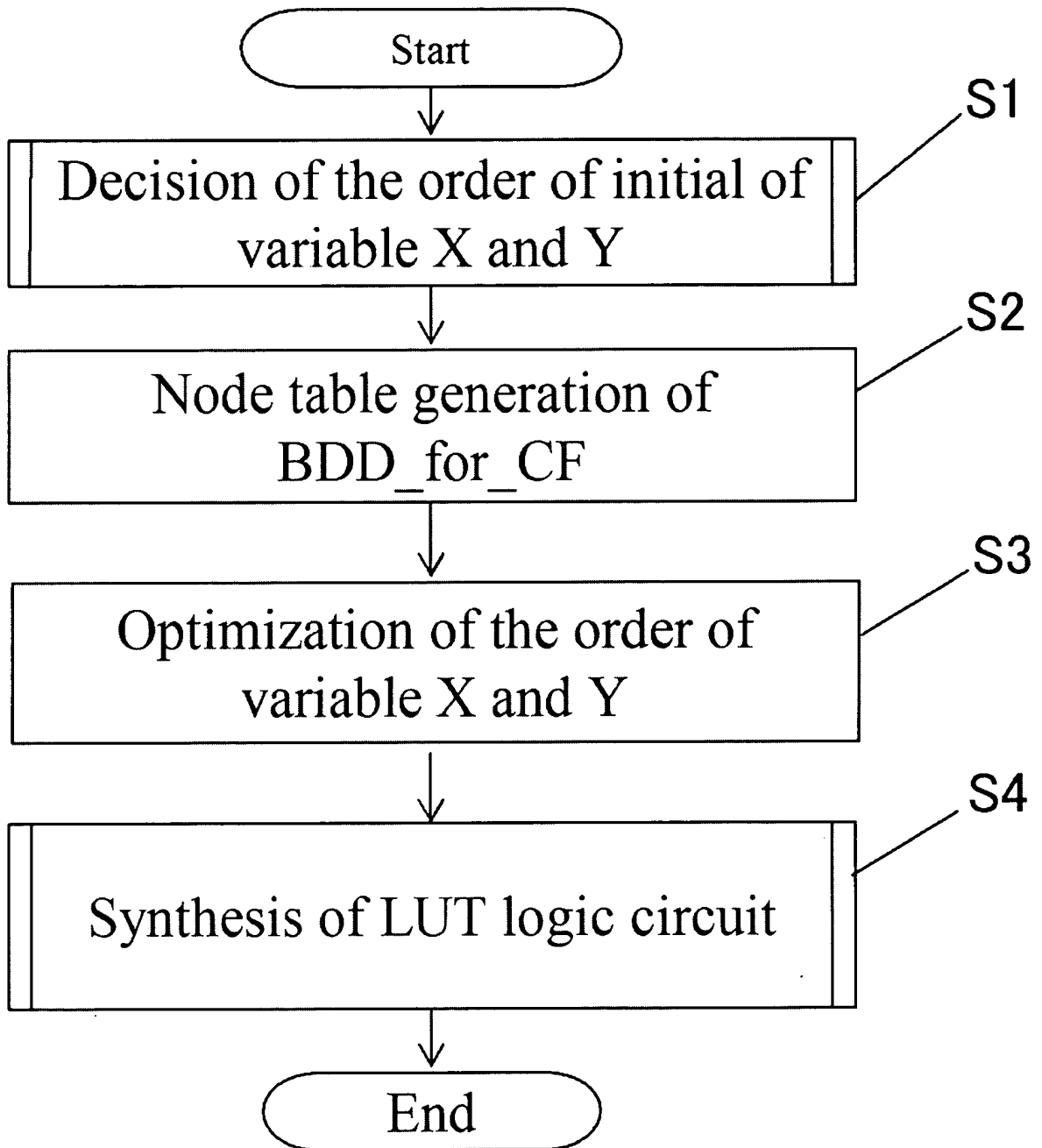


FIG. 10



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FIG. 11



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FIG. 12

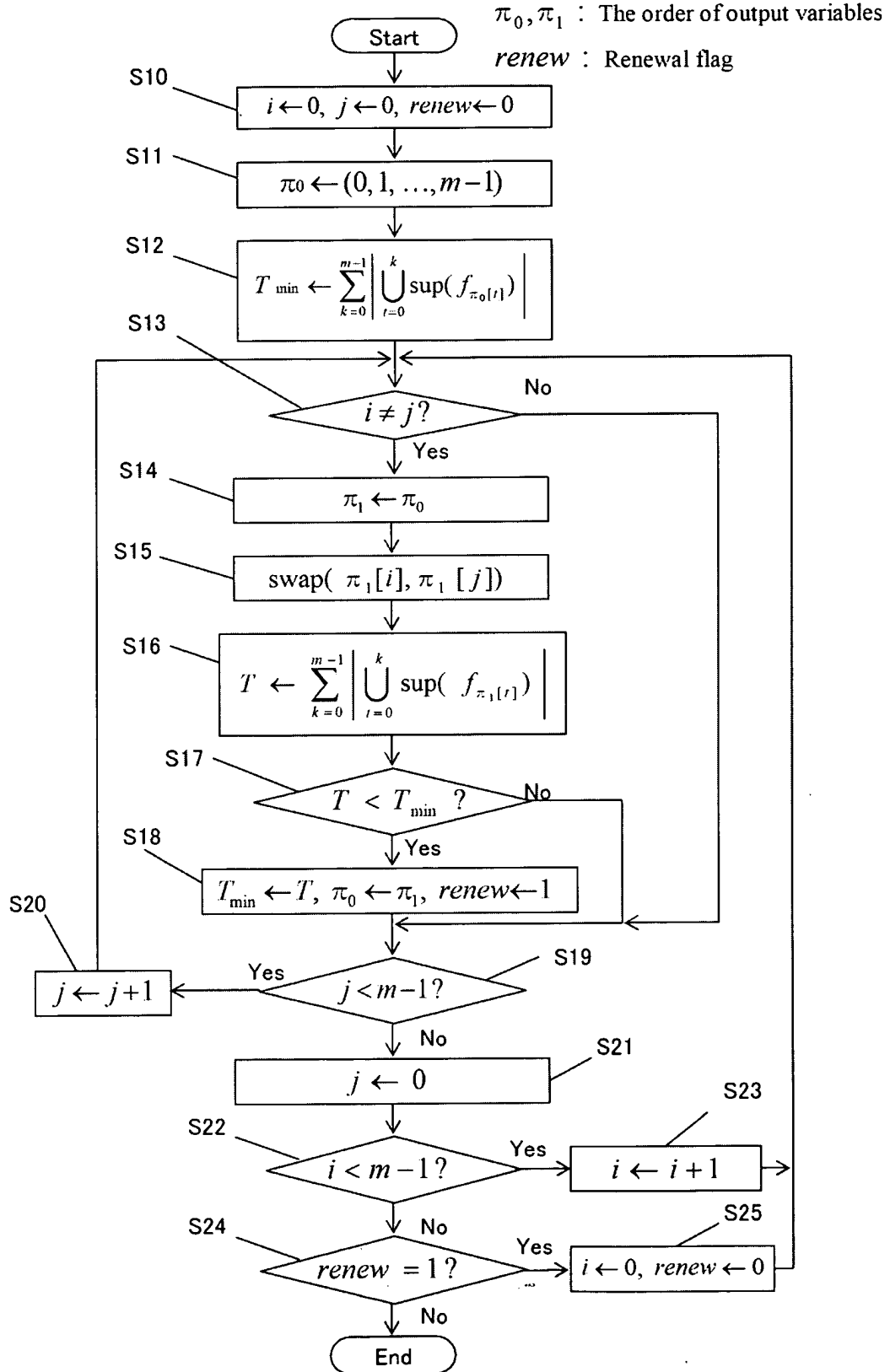


FIG. 13

Structure of node table

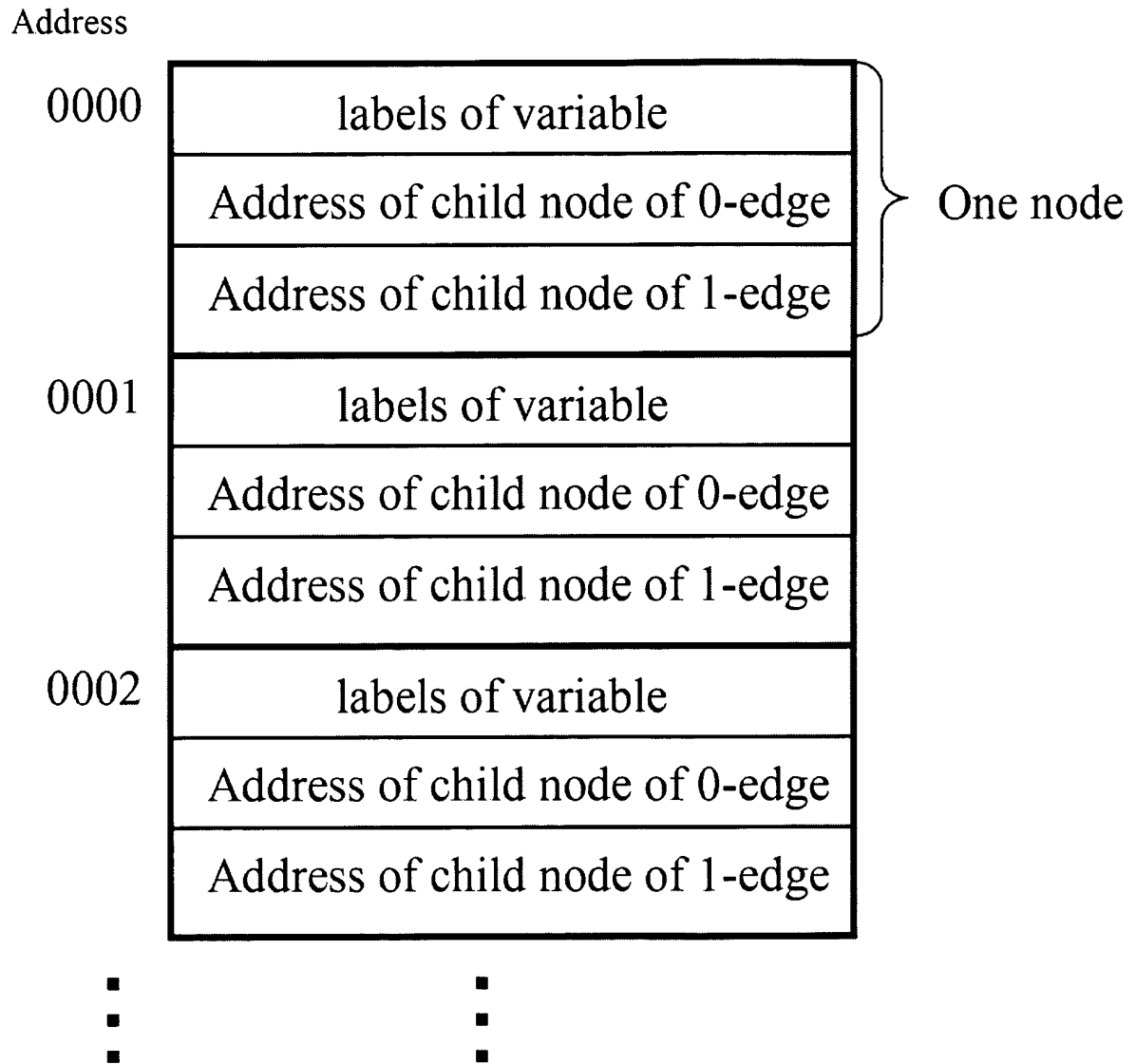


FIG. 14

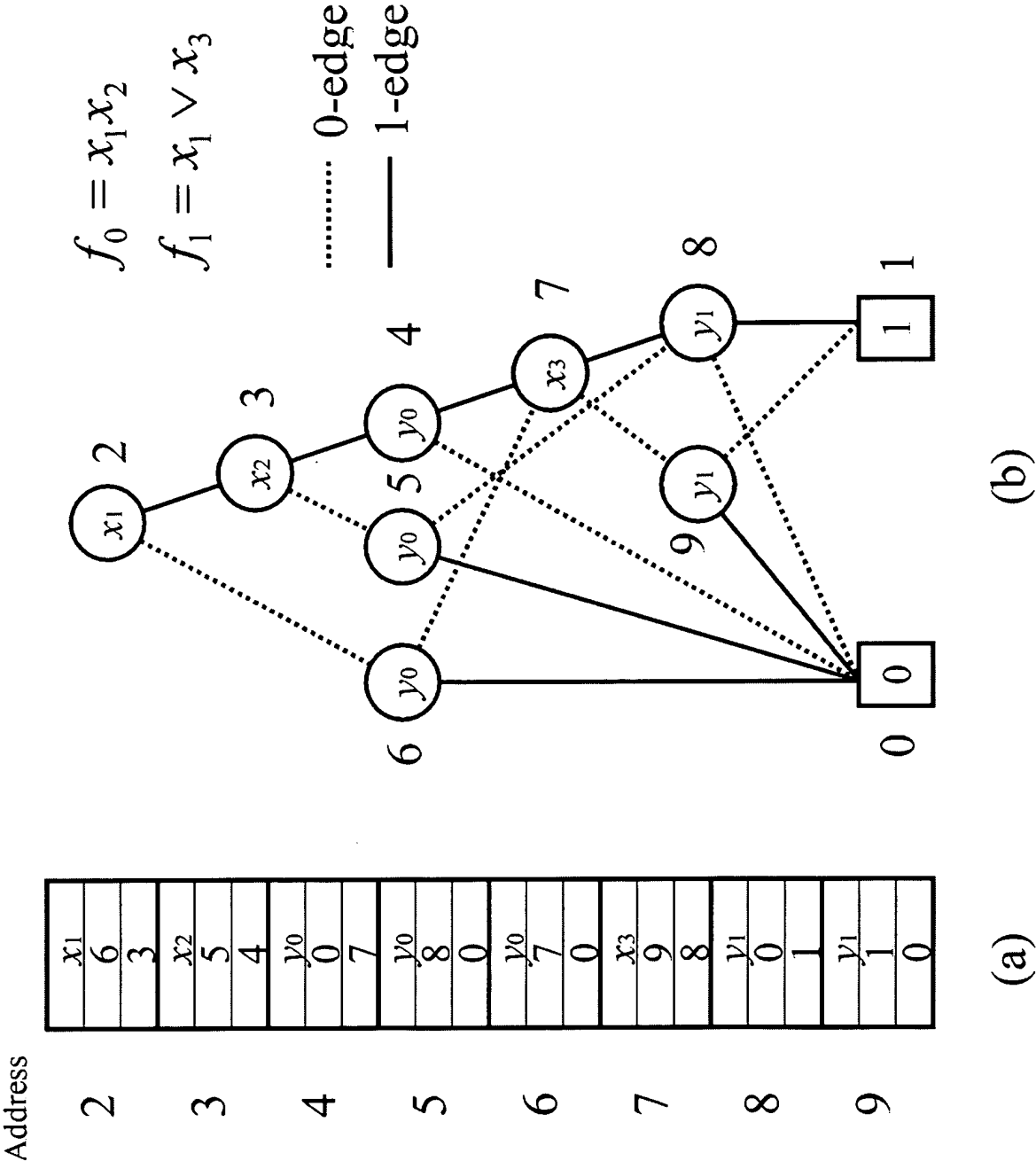


FIG. 15

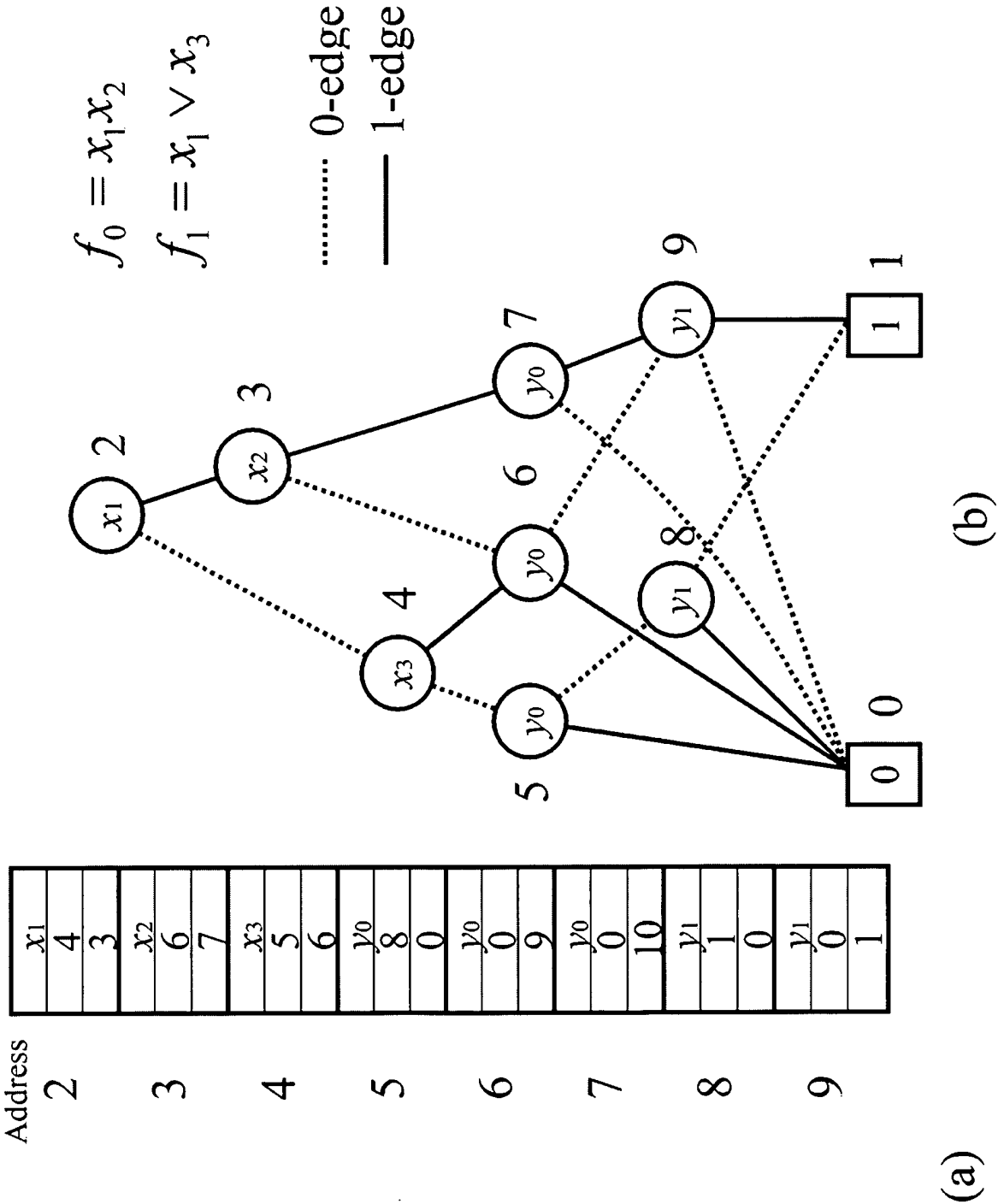


FIG. 16

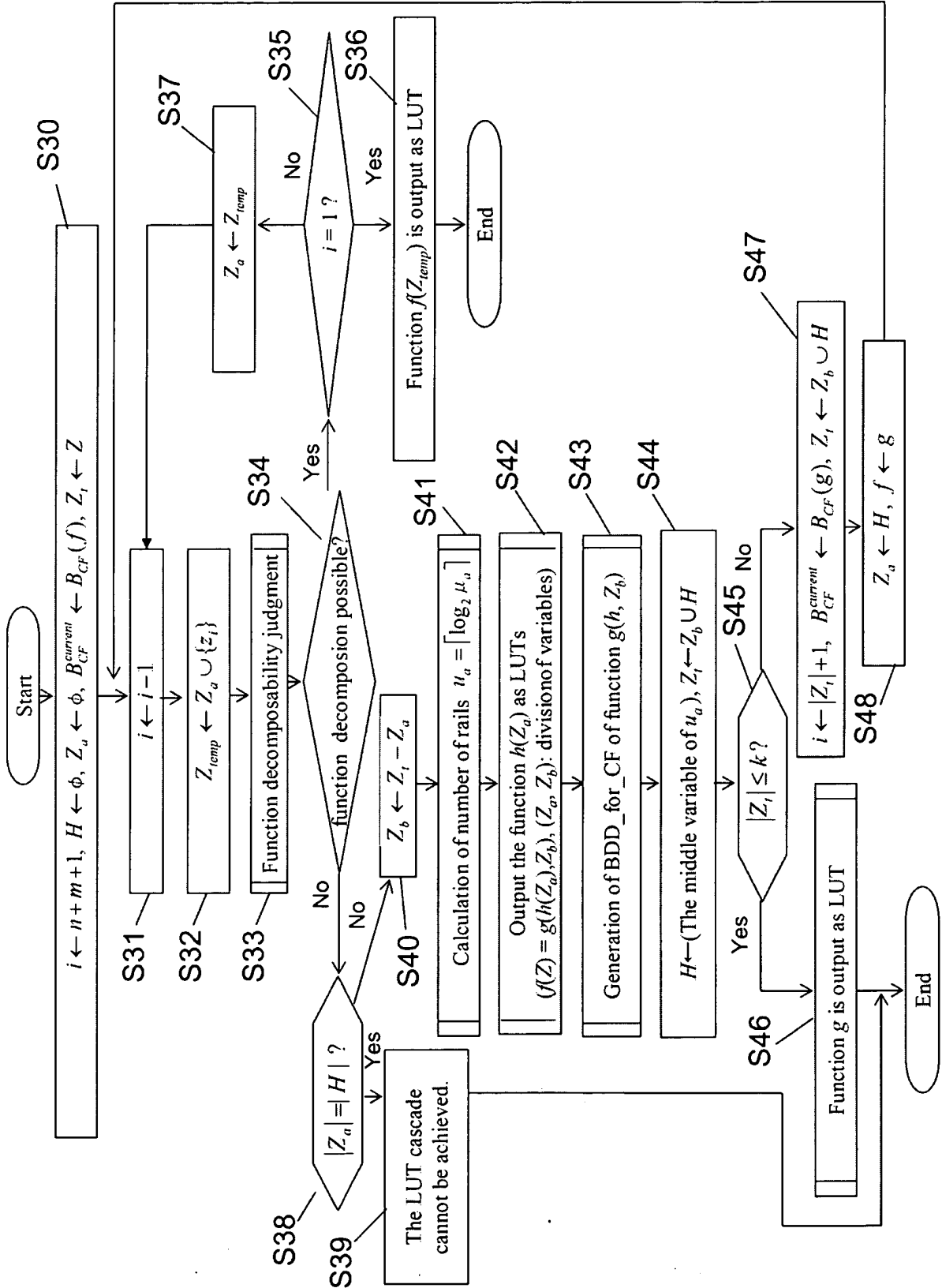


FIG. 17

Name	In	Out	method of present invention			Mishchenko [15]		
			LUT	Number of stages	Cas	LUT	Number of stages	Cas
C1908	33	25	320	11	5	3015	18	30
apex1	45	45	115	12	1	213	10	5
apex6	135	99	385	18	4	728	18	18
duke2	22	29	46	4	1	51	3	4
term1	34	10	50	8	1	37	6	2
tft2	24	21	54	6	1	47	4	3

Note : Number of input k of LUT as $k=10$.

FIG. 18

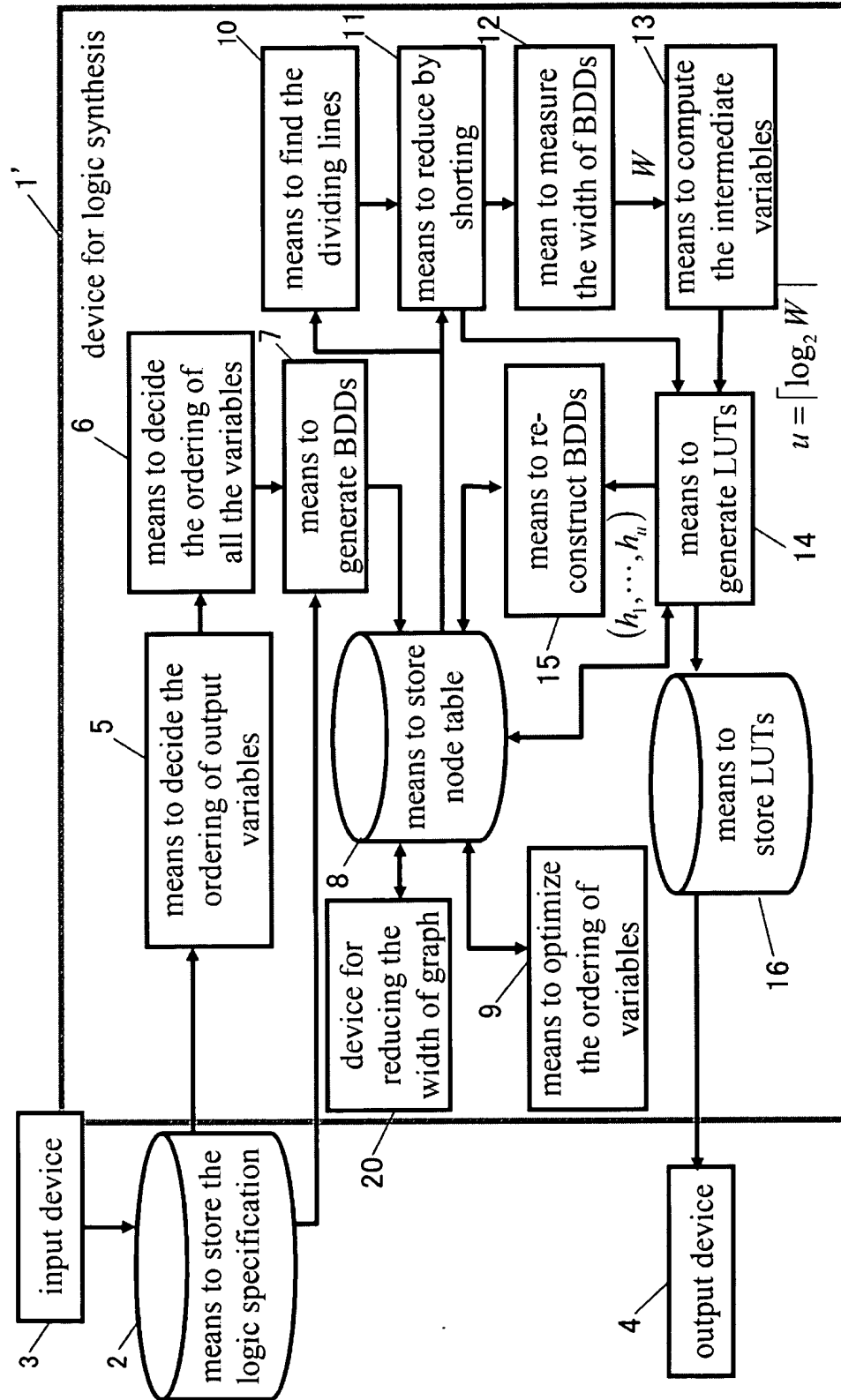


FIG. 19

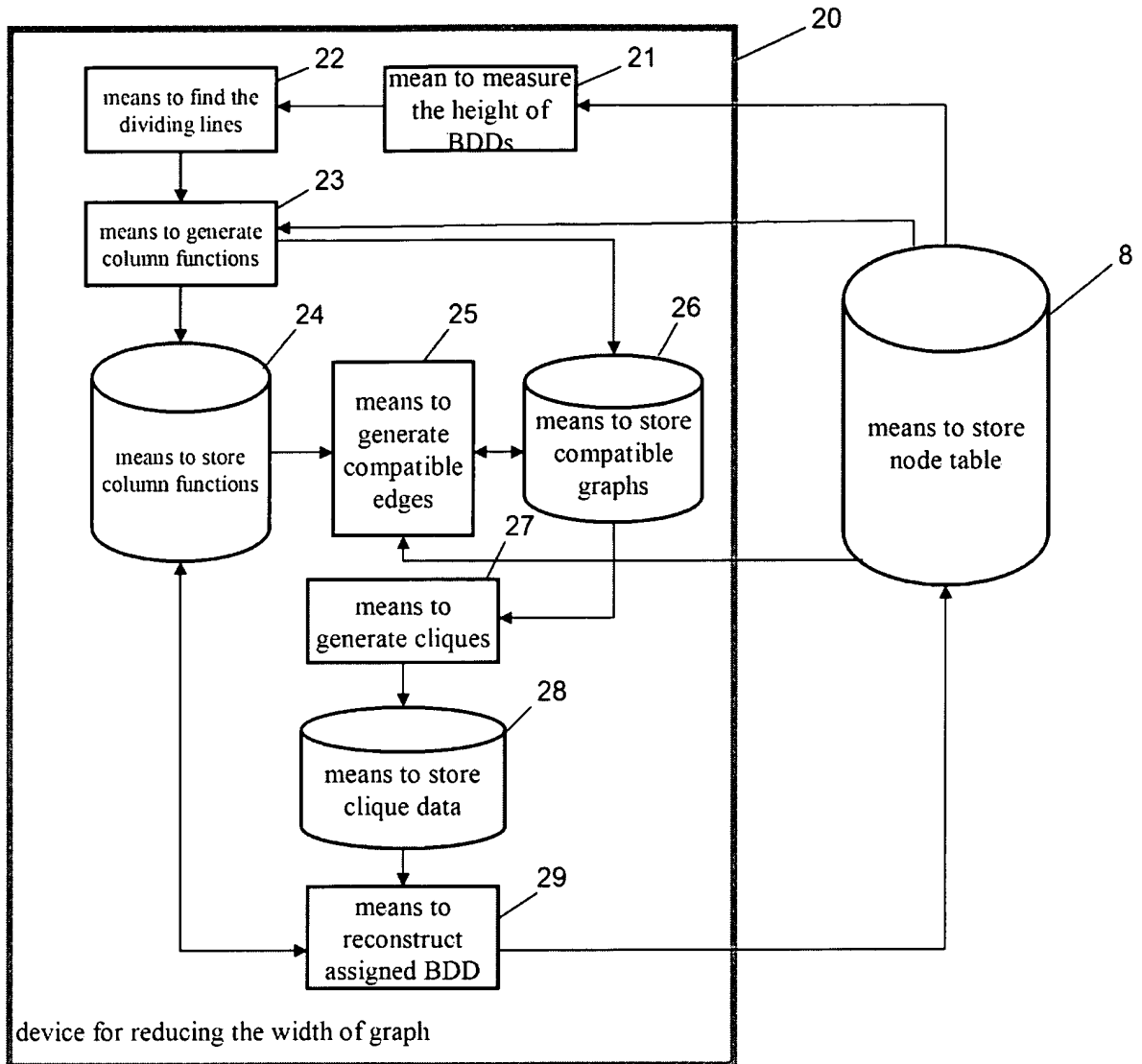


FIG. 20

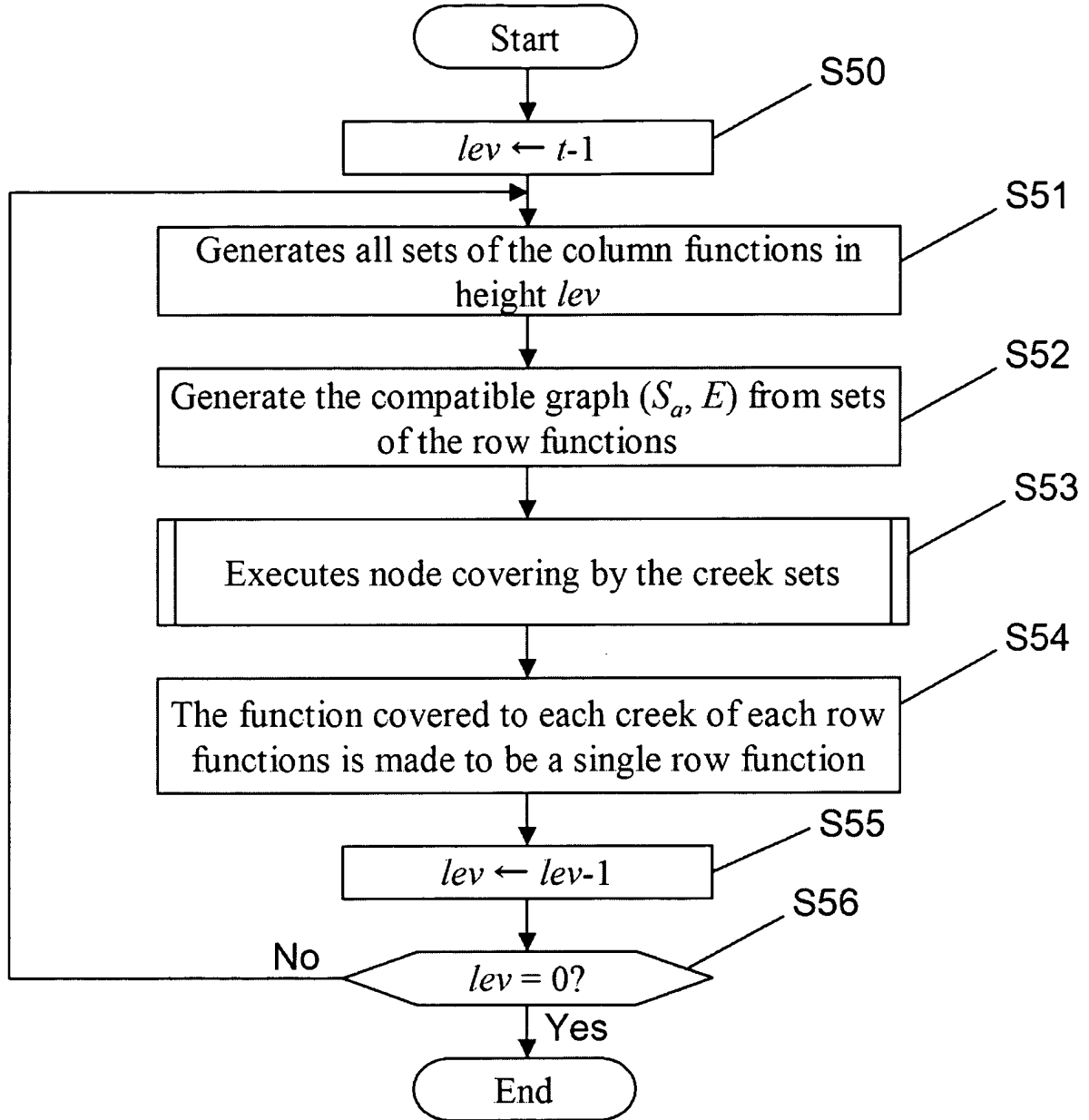


FIG. 21

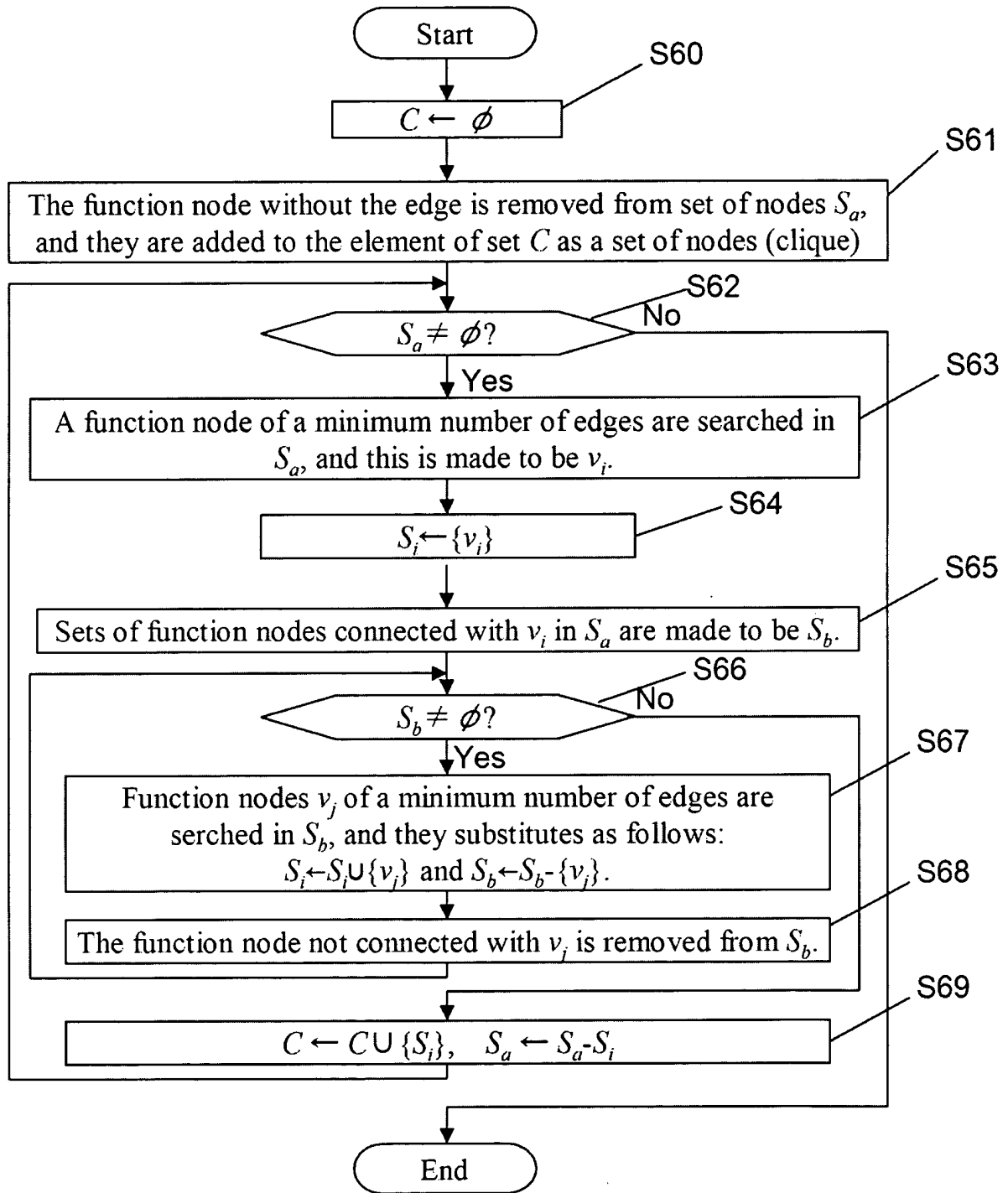


FIG. 22

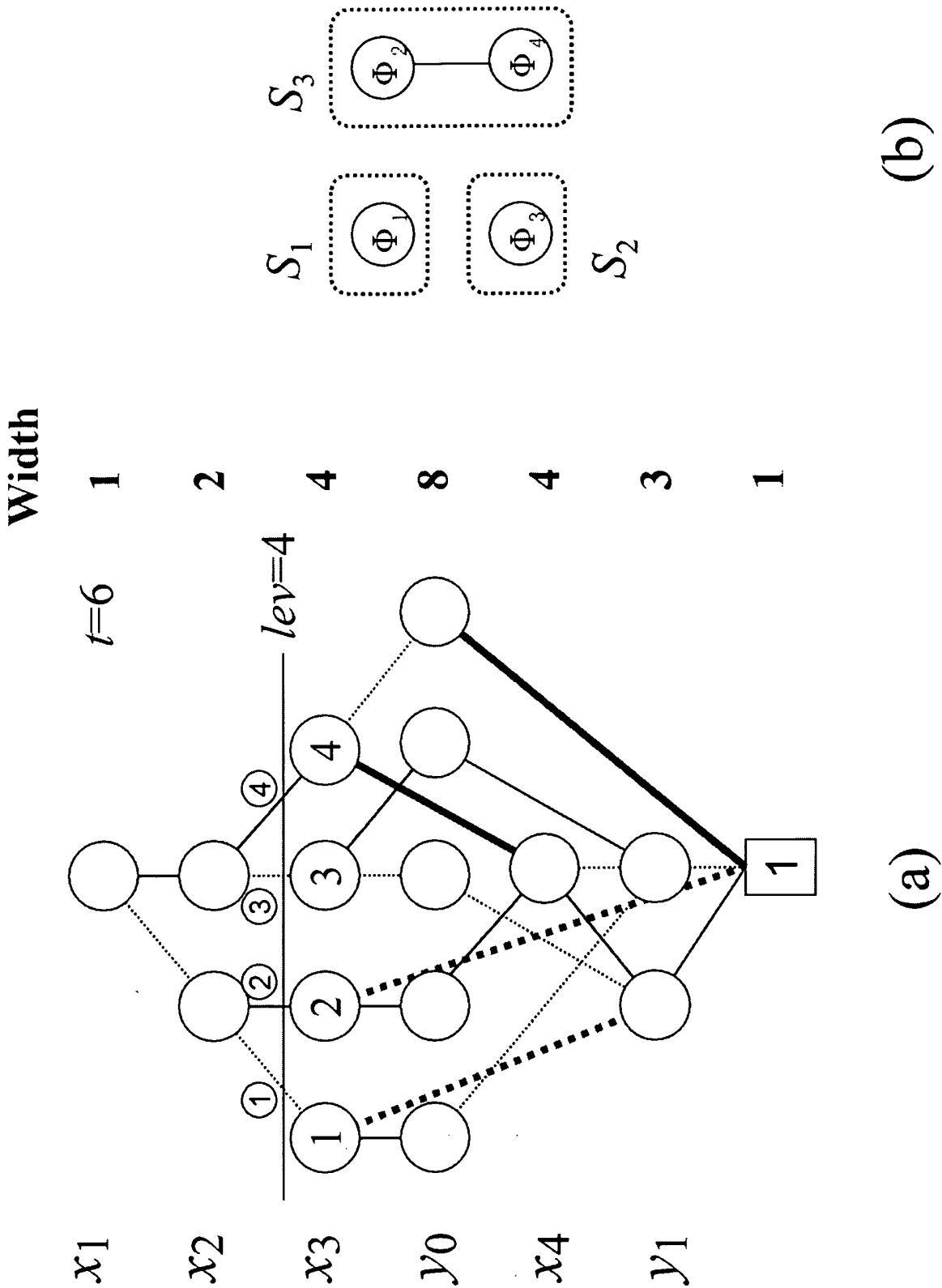
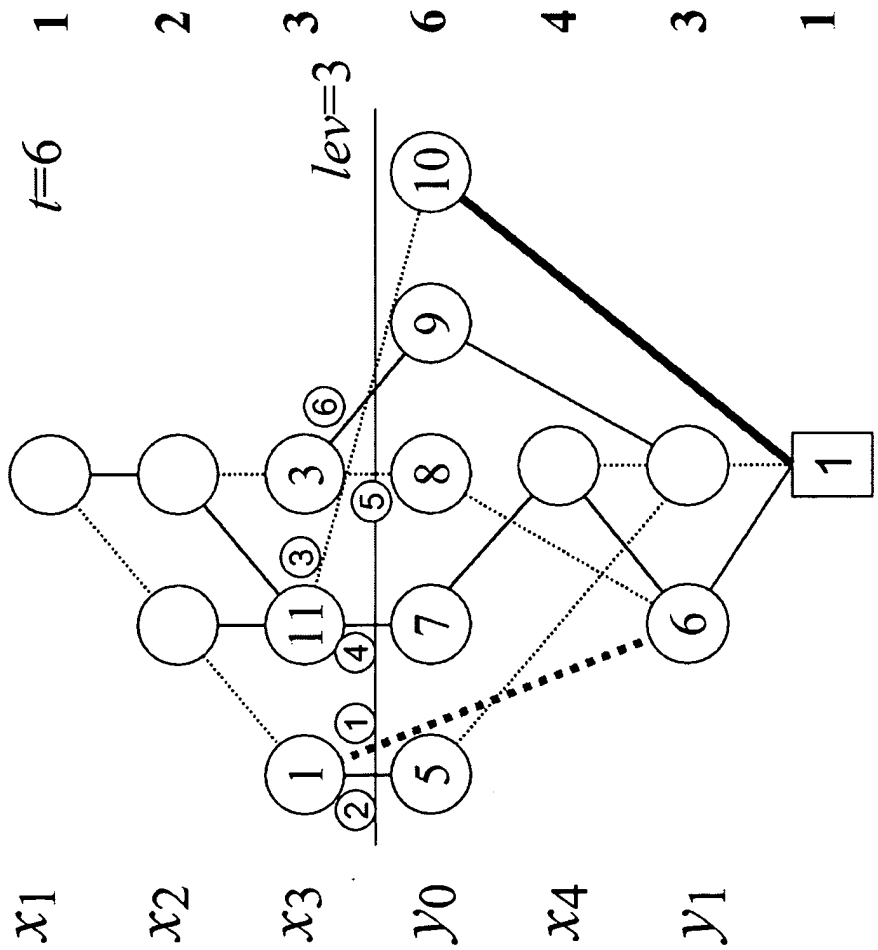
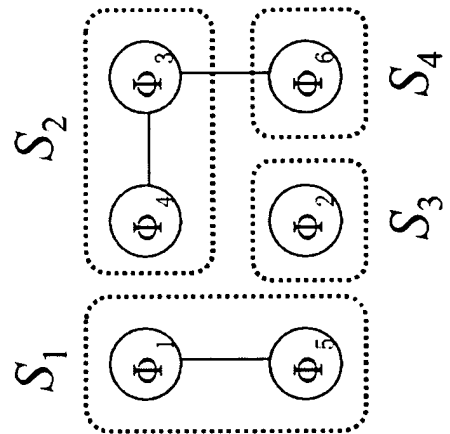


FIG. 23

Width



(a)



(b)

FIG. 24

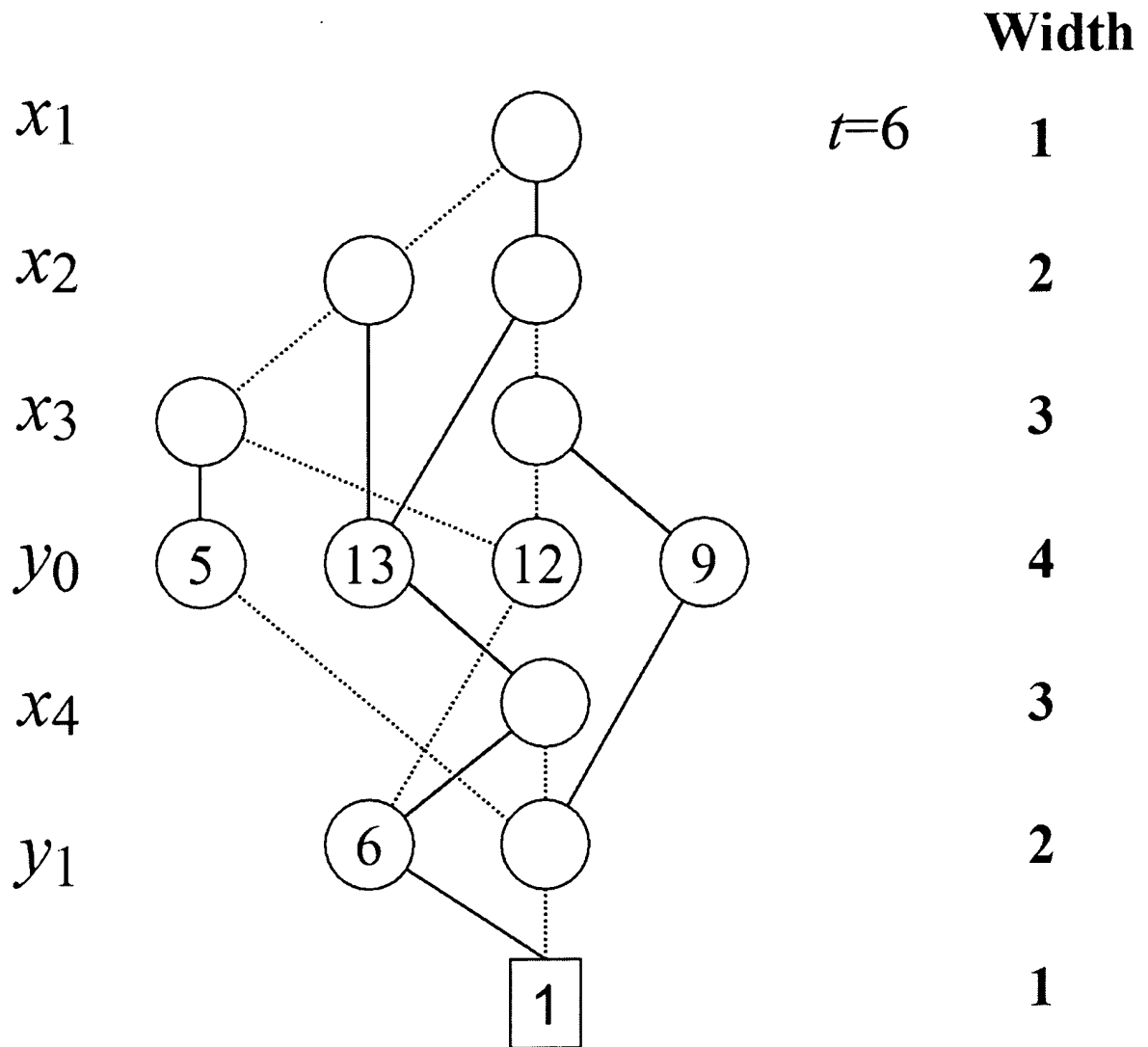
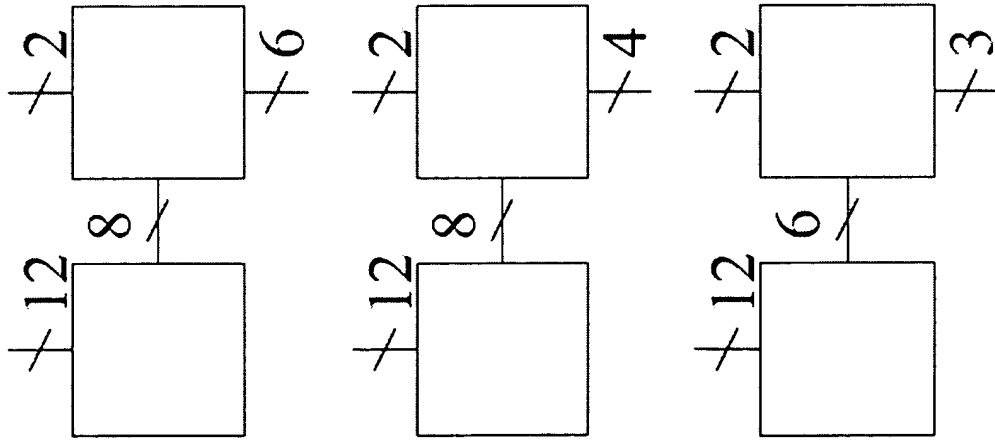
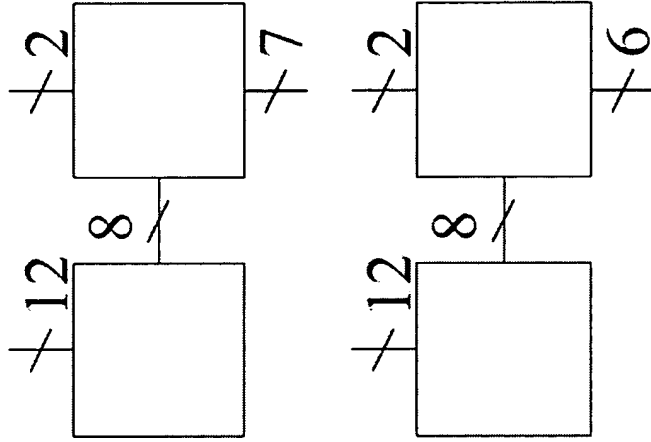


FIG. 25



(a)



(b)